



AF 41  
2188

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Dally et al.	Examiner:	Lane, J.
Serial No.:	09/394,222	Group Art Unit:	2188
Filed:	September 13, 1999	Docket No.:	STFD.073PA (S97-245)
Title:	System And Method For Re-Ordering Memory References In A Memory Control System To Speed Access To Memory Cells		

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 23, 2004.

By:

Erin M. Nichols

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Technology Center 2100

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the final Office Action dated June 21, 2004, please consider the following remarks.

The pending claims are reflected in the listing of claims beginning at page 2 and the Remarks begin on page 8. No amendments are presented herein.